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1 A Class B switch-mode assisted linear amplifier

Walker, G.R.;

Power Electronics, IEEE Transactions on , Volume: 18 , Issue: 6 , Nov. 2003
Pages:1278 - 1285
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2 Performance Analysis of Digital Integrate-and-Dump Filters

Chie, C.;

Communications, IEEE Transactions on [legacy, pre - 1988] , Volume: 30 , Issue: 8 , Aug 1982
Pages:1979 - 1983
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3 Operation of the 30 MJ superconducting magnetic energy storage system in the Bonneville Power Administration electrical grid

Rogers, J.; Boenig, H.; Schermer, R.; Hauer, J.;

Magnetics, IEEE Transactions on , Volume: 21 , Issue: 2 , Mar 1985
Pages:752 - 755
[\[Abstract\]](#) [\[PDF Full-Text \(552 KB\)\]](#) **IEEE JNL**
4 Analysis of leakage-inductance effect on characteristics of flyback converter without right half plane zero

Terashi, H.; Ninomiya, T.;

Telecommunications Energy Conference, 2003. INTELEC '03. The 25th International , 19-23 Oct. 2003
Pages:463 - 469

[\[Abstract\]](#) [\[PDF Full-Text \(349 KB\)\]](#) IEEE CNF

**5 A practical approach to harmonic compensation in power systems-s
connection of passive and active filters**

Fujita, H.; Akagi, H.;

Industry Applications Society Annual Meeting, 1990., Conference Record of th
1990 IEEE , 7-12 Oct. 1990

Pages:1107 - 1112 vol.2

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Result page: [1](#) [2](#) [next](#)Relevance scale ☐ ☐ ☐ ☐ ☐**1** [A distributed UNIX system based on a virtual circuit switch](#)


G. W.R. Luderer, H. Che, J. P. Haggerty, P. A. Kirsliis, W. T. Marshall

December 1981 **Proceedings of the eighth ACM symposium on Operating systems principles**Full text available:  [pdf\(801.12 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The popular UNIXTM operating system provides time-sharing service on a single computer. This paper reports on the design and implementation of a distributed UNIX system. The new operating system consists of two components: the S-UNIX subsystem provides a complete UNIX process environment enhanced by access to remote files; the F-UNIX subsystem is specialized to offer remote file service. A system can be configured out of many computers which operate either under the S-U ...

2 [Development and application of NASA's first standard spacecraft computer](#)

Charles E. Trevathan, Thomas D. Taylor, Raymond G. Hartenstein, Ann C. Merwarth, William N. Stewart

September 1984 **Communications of the ACM**, Volume 27 Issue 9Full text available:  [pdf\(1.26 MB\)](#)Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

To provide the autonomy needed by low, earth-orbiting satellites, NASA's first standard on-board processor requires changing only interfacing hardware from mission to mission.

Keywords: PASS, avionics system**3** [The SimpleScalar tool set, version 2.0](#)

Doug Burger, Todd M. Austin

June 1997 **ACM SIGARCH Computer Architecture News**, Volume 25 Issue 3Full text available:  [pdf\(985.46 KB\)](#)Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

This document describes release 2.0 of the SimpleScalar tool set, a suite of free, publicly available simulation tools that offer both detailed and high-performance simulation of modern microprocessors. The new release offers more tools and capabilities, precompiled binaries, cleaner interfaces, better documentation, easier installation, improved portability, and higher performance. This paper contains a complete description of the tool set, including retrieval and installation instructions, a d ...

- 4 (Special session) presentation + poster disscussion: university design contest: Design of POP-11 (PDP-11 on programmable chip)

Yoshihiro Iida, Naohiko Shimizu

January 2004 **Proceedings of the 2004 conference on Asia South Pacific design automation: electronic design and solution fair 2004**

Full text available:  pdf(107.91 KB) Additional Information: [full citation](#), [abstract](#), [references](#)

We developed POP-11/40 which is PDP-11/40 compatible processor, implemented it on an FPGA. We also designed ANSI C development tools an FPGA base-board which has SRAM, Serial and IDE interface, and run UNIX V6 on this system. In this paper, we describe a feature of a design of POP-11/40, base-board and UNIX V6.

- 5 The Kiewit network: a large AppleTalk internetwork

R. E. Brown

August 1987 **ACM SIGCOMM Computer Communication Review , Proceedings of the ACM workshop on Frontiers in computer communications technology,**
Volume 17 Issue 5

Full text available:  pdf(1.42 MB) Additional Information: [full citation](#), [abstract](#), [index terms](#)

Dartmouth College's Kiewit Network connects nearly all of the computing resources on the campus: mainframes, minicomputers, personal computers, terminals, printers, and file servers. It is a large internetwork, based on the AppleTalk protocols. There are currently over 2900 AppleTalk outlets in 44 zones on campus. Over 90 minicomputers act as bridges between 177 AppleTalk twisted pair busses. This paper describes the extent and facilities of the current network; the extensions made to the A ...

- 6 Using microcomputers to teach computer science

Alfred C. Weaver

January 1979 **ACM SIGCSE Bulletin , Proceedings of the tenth SIGCSE technical symposium on Computer science education,** Volume 11 Issue 1

Full text available:  pdf(277.99 KB) Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

This paper reports the author's progress toward developing a microcomputer laboratory suitable for teaching some of the basic concepts of computer science. The rationale for this approach is presented, followed by a description of the hardware and software environment thus far developed. Finally we present two course outlines: one for an undergraduate laboratory course in computer systems and another for a graduate course in microcomputer systems design for engineers.

- 7 PixelFlow: the realization

John Eyles, Steven Molnar, John Poulton, Trey Greer, Anselmo Lastra, Nick England, Lee Westover

August 1997 **Proceedings of the ACM SIGGRAPH/EUROGRAPHICS workshop on Graphics hardware**

Full text available:  pdf(1.54 MB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

Keywords: compositing, deferred shading, object-parallel, rendering, scalable

- 8 Contributed articles: APL workspace transfer

Francois Lapadu-Hargues

March 1978 **ACM SIGAPL APL Quote Quad,** Volume 8 Issue 3

Full text available:  pdf(517.67 KB) Additional Information: [full citation](#), [references](#)

9 Predictability requirements of a soft modem

Michael B. Jones, Stefan Saroiu

June 2001 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 2001 ACM SIGMETRICS international conference on Measurement and modeling of computer systems**, Volume 29 Issue 1

Full text available:  pdf(1.53 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

Soft Modems use the main processor to execute modem functions traditionally performed by hardware on the modem card. To function correctly, soft modems require that ongoing signal processing computations be performed on the host CPU in a timely manner. Thus, signal processing is a commonly occurring background real-time application---one running on systems that were not designed to support predictable real-time execution. This paper presents a detailed study of the performance characteris ...

Keywords: CPU scheduling, Rialto, Rialto/NT, Windows 2000, Windows NT, open real-time system, real-time, signal processing, soft devices, soft modem

10 Using the Alfa-1 simulated processor for educational purposes

Gabriel A. Wainer, Sergio Daicz, Luis F. De Simoni, Demian Wassermann

December 2001 **Journal on Educational Resources in Computing (JERIC)**, Volume 1 Issue 4

Full text available:  pdf(238.65 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Alfa-1 is a simulated computer designed for computer organization courses. Alfa-1 and its accompanying toolkit allow students to acquire practical insights into developing hardware by extending existing components. The DEVS formalism is used to model individual components and to integrate them into a hierarchy that describes the detailed behavior of different levels of a computer's architecture. We introduce Alfa-1 and the toolkit, show how to extend existing components, and describe how ...

Keywords: DEVS formalism, modeling computer architectures, systems specification

11 Data filtering applied to information storage and retrieval applications

Robert P. Larsen

November 1966 **Communications of the ACM**, Volume 9 Issue 11


Full text available:  pdf(857.36 KB) Additional Information: [full citation](#), [abstract](#), [references](#)

Manipulation of data strings is the most complex processing function in information storage and retrieval applications. Data string manipulation is discussed within the context of an interpretive processing environment controlled by the use of procedural directives. The sequence of procedural directives is derived from a job assumed to be expressed in a user-oriented-source language. Each data string within the structured data environment (data bank) is explicitly or implicitly r ...

12 An input interface for a real-time digital sound generation system

Paul E. Dworak, Alice C. Parker

January 1976 **ACM SIGARCH Computer Architecture News , Proceedings of the 3rd annual symposium on Computer architecture**, Volume 4 Issue 4

Full text available:  pdf(441.54 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)


A man/computer input interface is described in this paper. This interface allows human control of the frequency, amplitude, spectral content and envelope of real-time sound production. The interface consists of a two-dimensional array of keys for entering data, latches and comparators to signal changes in key depression, hardware for scanning the

keyboard and address generation logic. Both the fundamental frequency represented by each key column and the harmonics of each frequenc ...

13 Interaction in the real world: Ambient touch: designing tactile interfaces for handheld devices

Ivan Poupyrev, Shigeaki Maruyama, Jun Rekimoto

October 2002 **Proceedings of the 15th annual ACM symposium on User interface software and technology**

Full text available:  [pdf\(3.71 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)


This paper investigates the sense of touch as a channel for communicating with miniature handheld devices. We embedded a PDA with a TouchEngine™ --- a thin, miniature lower-power tactile actuator that we have designed specifically to use in mobile interfaces (Figure 1). Unlike previous tactile actuators, the TouchEngine is a universal tactile display that can produce a wide variety of tactile feelings from simple clicks to complex vibrotactile patterns. Using the TouchEngine, we bega ...

Keywords: mobile devices and interfaces, tactile feedback

14 The PORT Mathematical Subroutine Library

P. A. Fox, A. P. Hall, N. L. Schryer

June 1978 **ACM Transactions on Mathematical Software (TOMS)**, Volume 4 Issue 2

Full text available:  [pdf\(1.60 MB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

15 The open shop interactive mask design operation at harris semiconductor

Justin E. Harlow, Harris Semiconductor, Palm Bay Florida

January 1977 **Proceedings of the 14th conference on Design automation**

Full text available:  [pdf\(239.60 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [index terms](#)

This paper discusses the evolution of a unique setup for design of integrated circuit masks, using interactive graphics equipment in an open shop environment. The evolution of this design philosophy is traced from manual drafting and digitizing, through limited computer editing, to the present drawing-free methods. Implementation and continuing operation of the facility have posed many unusual problems of load distribution, priority management, control of users and procedures, and data inte ...

16 Use of simulation in support of development and testing of submarine subsystems

James O. Goodburn, Robert A. Massarotti

March 1978 **Proceedings of the 11th annual symposium on Simulation**

Full text available:  [pdf\(512.70 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [index terms](#)

The purpose of this paper is to describe an existing real-time digital simulation which is utilized in the testing and integration of submarine subsystems. The testing and integration effort involves both the hardware and software verification. Advanced fire control systems are not only composed of new hardware but also larger and more sophisticated software systems. The only way to test and check-out these systems in a laboratory environment is with simulation. The simulation provides dyna ...

17 Polyglot Emacs 20.4: A look at multilingual Emacs

Jon Babcock

March 1999 **Linux Journal**

Full text available:  [html\(23.41 KB\)](#) Additional Information: [full citation](#), [references](#), [index terms](#)

18 [Substrate crosstalk analysis in mixed signal CMOS integrated circuits: embedded tutorial](#)



Makoto Nagata, Atsushi Iwata

January 2000 **Proceedings of the 2000 conference on Asia South Pacific design automation**

Full text available:  [pdf\(476.00 KB\)](#) Additional Information: [full citation](#), [references](#)

19 [A mini-micro computer link promotes distributed processing in scientific and engineering computations](#)



Shmuel Gruber, Meir Tahan, Michael Z. Hanani

August 1978 **Proceedings of the first SIGMINI symposium on Small systems**

Full text available:  [pdf\(443.31 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Engineering and scientific work often requires complex computations of different types that should be carried out on different types of computers on the one hand, but must communicate and support each other on the other. To facilitate efficient execution of such computations, a network composed of mini and micro computers (DEC PDP 11/40 and Intel 8080) with the support of a large computer (CDC CYBER 73) has been built and is described in this paper. The network enables the realization of in ...

Keywords: Computer linkage, Distributed processing, Microcomputer, Minicomputer, Network, Telecommunication

20 [Production: The unreal editor as a Web 3D authoring environment](#)



David Arendash

April 2004 **Proceedings of the ninth international conference on 3D Web technology**

Full text available:  [pdf\(638.49 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Epic Games provides a free game level editor with titles based on its Unreal engine. The editor provides a rich set of authoring tools that can be used to create fully interactive environments. This paper describes a tool that converts Unreal levels to web-ready environments in VRML and X3D. The paper also examines the similarities between first-person-shooter games and web 3D worlds, and discusses the implications of having a low-cost, fully featured virtual world authoring environment available ...



Keywords: 3D, Unreal, VRML, X3D, authoring, game, tool

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